

PIC Microcontroller Hardware Peripherals

DATE: 7/14/17

TIME: 9:00 am to 4:00 pm

LOCATION: Microchip Technology Regional Training Center, 112 Turnpike Road, Suite 100, Westborough Executive Park, Westborough, Massachusetts 01581

COURSE DESCRIPTION: Overview of many of the Microchip PIC's established, new, and unusual peripherals built into their families of microcontrollers. Discussion will cover classic as well as innovative uses

OUTLINE:

- The Configurable Logic Cell (CLC)
- The Numerically Controlled Oscillator (NCO)
- The Complimentary Output Generator (COG)
- Peripheral Pin Select (PPS)
- Signal Measurement Timer (SMT)
- Cyclic Redundancy Check
- Zero Cross Detect (ZCD)
- Windowed Watch Dog Timer
- Slope Compensation
- A/D
- Capacitive Touch (CTMU)
- 16-bit PWM Module
- Hardware Event Timer

MATERIALS: A CD containing a PDF of the presentation and copies of some FREE software tools.

PREREQUISITES: Basic familiarity with microprocessors

REQUIRED: Payment 1 week in advance.

FEE: \$299.00 per person.

ACCEPTED PAYMENT: Check, PayPal, or Credit Card.

TO REGISTER: Email Mchipguru@gmail.com. You will receive instructions on how to provide payment information and available discounts.